

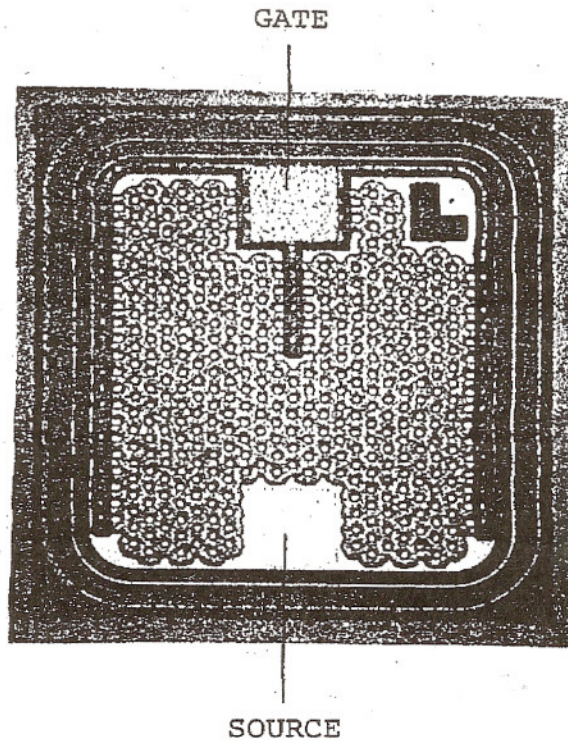


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Chip back potential is the level which bulk silicon is maintained by on-chip connection, or it is the level to which the chip back must be connected when specifically stated below. If no potential is given the chip back should be isolated.



CHIP BACK IS DRAIN

Topside Metal: Al
Backside: Au
Backside Potential: Drain
Mask Ref: Issue 2
Bond Pads: .006" min

APPROVED BY: CB

MFG: Harris

DIE SIZE: .046" x .046"

THICKNESS: .015"

DATE: 2/8/01

P/N: IRFC1Z0

DG 10.1.2
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